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## Vishay General Semiconductor

# High Current Density Surface Mount TMBS® (Trench MOS Barrier Schottky) Rectifier

Ultra Low  $V_F = 0.26 \text{ V}$  at  $I_F = 5 \text{ A}$ 



**LINKS TO ADDITIONAL RESOURCES** 

## **FEATURES**

- Very low profile typical height of 1.1 mm
- Ideal for automated placement
- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **TYPICAL APPLICATIONS**

For use in low voltage high frequency DC/DC converters, freewheeling, and polarity protection applications.

#### **MECHANICAL DATA**

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test

3 0				
3D Models				

PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	15 A			
$V_{RRM}$	50 V			
I <sub>FSM</sub>	200 A			
$V_F$ at $I_F = 15 A$	0.41 V			
T <sub>J</sub> max.	150 °C			
Package	SMPC (TO-277A)			
Circuit configuration	Single			

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V15PN50	UNIT	
Device marking code		15N5		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	50	V	
Maximum average forward rectified current (fig. 1)	I <sub>F</sub> <sup>(1)</sup>	15	Α	
	I <sub>F</sub> <sup>(2)</sup>	6.0		
Maximum DC reverse voltage	V <sub>DC</sub>	35	V	
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	200	А	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-40 to +150	°C	

#### **Notes**

- $^{(1)}$  Mounted on 30 mm x 30 mm 2 oz. pad PCB
- (2) Free air, mounted on recommended copper pad area



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 25 °C		0.38	-		
	I <sub>F</sub> = 7.5 A			0.41	-		
	I <sub>F</sub> = 15 A		V <sub>F</sub> <sup>(1)</sup>	0.48	0.56	V	
	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 125 °C		0.26	-		
	I <sub>F</sub> = 7.5 A		T <sub>A</sub> = 125 °C	. <sub>A</sub> = 125 °C	0.31	-	
	I <sub>F</sub> = 15 A			0.41	0.50	]	
Reverse current	$V_R = 50 \text{ V}$ $T_A = 25 \text{ °C}$ $T_A = 125 \text{ °C}$	T <sub>A</sub> = 25 °C	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	140	3000	μΑ
		IR (=)	60	140	mA		

#### Notes

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 5 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	V15PN50	UNIT		
Typical thermal resistance	R <sub>θJA</sub> (1) (2)	70	°C/W		
Typical trieffial resistance	R <sub>0JM</sub> (3)	4	0/ • •		

#### **Notes**

- $^{(1)}$  Free air, mounted on recommended copper pad area; thermal resistance  $R_{\theta JA}$  junction to ambient
- $^{(2)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$
- (3) Mounted on 30 mm x 30 mm 2 oz. pad PCB; thermal resistance R<sub>0JM</sub> junction to mount measured at cathode side

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V15PN50-M3/86A	0.10	86A	1500	7" diameter plastic tape and reel	
V15PN50-M3/87A	0.10	87A	6500	13" diameter plastic tape and reel	



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## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

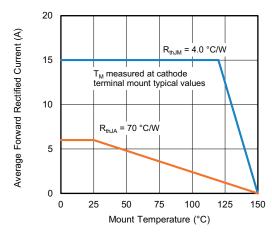


Fig. 1 - Maximum Forward Current Derating Curve (D = Duty Cycle = 0.5)

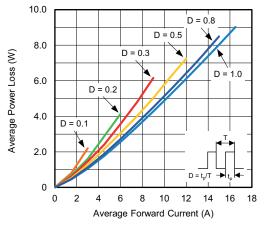


Fig. 2 - Forward Power Loss Characteristics

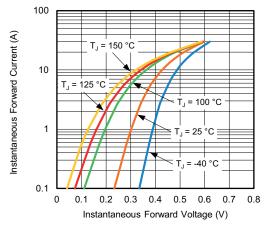


Fig. 3 - Typical Instantaneous Forward Characteristics

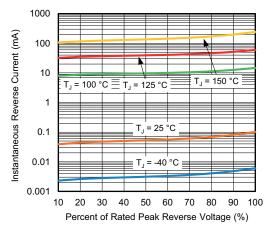


Fig. 4 - Typical Reverse Leakage Characteristics

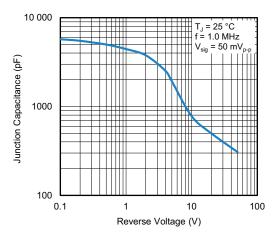


Fig. 5 - Typical Junction Capacitance

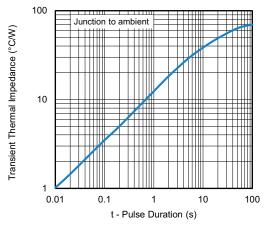
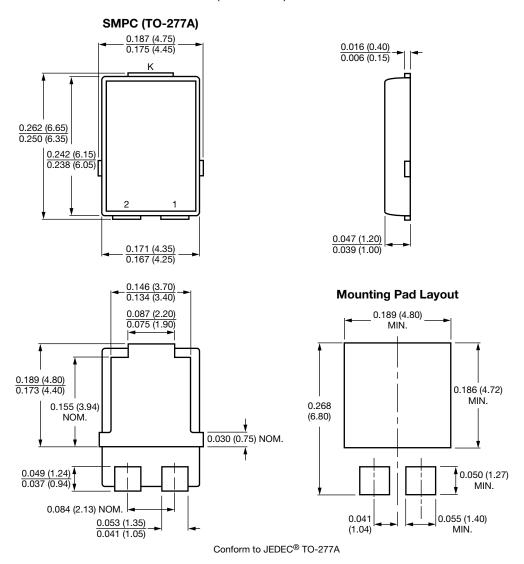


Fig. 6 - Typical Transient Thermal Impedance



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### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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